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--26. (New) The computer program product of claim 25 wherein the computer readable program code further includes:

computer readable program code capable of generating functional fail data for each of the integrated circuits.--  
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--27. (New) The computer program product of claim 25 wherein the test program includes test vectors and pin assignments for testing the integrated circuits in parallel.--

REMARKS

Claims 1-18 have been cancelled. Claims 19-27 have been added. New claims 19-27 have been drafted to avoid the concerns previously expressed by the Examiner concerning 35 U.S.C. Section 112.

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**CITED REFERENCES**

In the current Office Action, the Examiner rejected claims 1-18 as being obvious in view of U.S. Patent No. 6,331,770 to Sugamori et al. (Sugamori). Applicant's will discuss the critical differences between Sugamori and Applicant's present invention as now defined by the pending claims below.

**Sugamori**

Sugamori discloses a semiconductor testing system intended to overcome the deficiencies associated with having the system designed to test the fastest and most advanced integrated circuits available (Background). Sugamori's invention capitalized on the realization that it would be more efficient if the system could be designed generically to test both low end and high end integrated circuits, and if the pins of the system could be grouped together to perform a dedicated function (e.g. Analog or digital) (Figs 4-6 and the associated description).

Although Sugamori has the ability to test different portions of the integrated circuit in parallel using this grouping via the tester modules, Sugamori fails to disclose, teach or suggest testing multiple integrated circuits in parallel as now claimed by Applicants. More specifically, Applicants' generate a test environment that can convert an integrated circuit tester that has been designed to test a **single integrated circuit**, such as Sugamori, to test a plurality of integrated circuits in parallel.

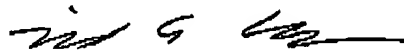
**SUMMARY AND CONCLUSION**

In view of the foregoing, withdrawal of the rejections and the allowance of the current pending claims is respectfully requested. If the Examiner feels that the pending claims could be

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allowed with minor changes, the Examiner is invited to telephone the undersigned to discuss an Examiner's Amendment. If there are any additional fees due in connection with the filing of this amendment, please charge the fees to undersigned's Deposit Account No. 09-0456. If any extensions or fees are not account for, such extension is requested and the associated fee should be charged to our deposit account.

Respectfully submitted,

Date: 7-15-03

BY: Richard A. Henkler  
Attorney for Applicants  
Registration No. 39,220  
(802) 769-8585